

What is claimed is:

1. A semiconductor memory device comprising:
an encoding means for encoding input data
according to a predetermined error correction encoding
5 system;
a comparing means for comparing said input data
and a predetermined status data;
a selecting means for selecting either of said
input data or encoded data output from said encoding
10 means in accordance with a comparison result of said
comparing means; and
a nonvolatile memory for storing data selected
by said selecting means,
wherein said nonvolatile memory holds said
15 status data in a predetermined initialization state.
2. A semiconductor memory device as set forth in
claim 1, wherein said nonvolatile memory holds said
status data in an erasure state.
3. A semiconductor memory device as set forth in
20 claim 1, wherein said selecting means selects said status
data when said input data coincides with said status data
and selects said encoded data output from said encoding
means when said input data does not coincide with said
status data.
- 25 4. A semiconductor memory device comprising:

a decoding means for decoding data read from a nonvolatile memory according to a predetermined error correction decoding system;

a comparing means for comparing data input to
5 said decoding means and predetermined status data; and

a selecting means for selecting either of said status data or decoded data output from said decoding means in accordance with a comparison result of said comparing means,

10 wherein said nonvolatile memory holds said status data in a predetermined initialization state.

5. A semiconductor memory device as set forth in claim 3, wherein said selecting means selects said status data when said read data coincides with said status data
15 and selects said decoded data when said read data does not coincide with said status data.

6. A semiconductor memory device for error correction encoding/decoding input/output data, comprising:

20 an encoding means for encoding input data according to a predetermined error correction encoding system;

a comparing means for comparing said input data and predetermined status data;

25 a first selecting means for selecting either of

said input data or encoded data output from said encoding means in accordance with a comparison result of said comparing means;

a nonvolatile memory for storing data selected
5 by said selecting means;

a decoding means for decoding data read from said nonvolatile memory according to a predetermined error correction decoding system;

a comparing means for comparing the decoded
10 data output by said decoding means and said status data;
and

a second selecting means for selecting either of said status data or said decoded data in accordance with the comparison result of said comparing means,
15 wherein said nonvolatile memory holds said status data in a predetermined initialization state.

7. A semiconductor memory device as set forth in claim 6, wherein said nonvolatile memory holds said status data in an erasure state.